



2838
JL

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: James M. Thommes
Serial No. : 09/827,440
Filing Date : April 6, 2001
For : Method And Apparatus For Receiving A
Universal Input Voltage In A Welding
Power Source
Attorney Docket No. : ITW 7188.62

Disclosure of Prior Art, Litigation, and Litigation Documents

Honorable Commissioner of
Patents and Trademarks
Washington, D. C. 20231


Dear Sir:

Enclosed is a form 1449 and cited art.

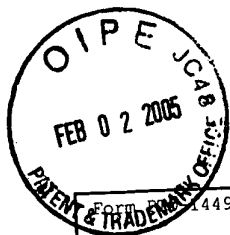
Also, Applicant is hereby notifying the Office that at least some of the subject matter being claimed is the subject of an existing litigation (Case No. 1:03-CV-00966, ED Wis), wherein the assignee Illinois Tool Works Inc. has sued Thermal Dynamics Corporation. Applicant is including copies of papers from that litigation, including:
Thermal Dynamics' Supplemental Answers To Illinois Tool Works Inc. And Miller Elec. Mfg. Co.'s Interrogatories Nos. 3-6, 8 And 13-14

These documents are being provided because the Defendant has asserted the parent patents are invalid.

Respectfully Submitted


George R. Corrigan, Reg. No. 34,803
Corrigan Law Office
5 Briarcliff Court
Appleton, WI 54915
(920) 954-1099

*DN*C:\WP51\DOCS\ITW\7188.63\Disclosurecvr.frm Friday, July 30, 2004 12:52PM



Sheet 1 of 1.

U.S. Department of Commerce Patent And Trademark Office		ATTY. DOCKET NO. ITW 7188.64		SERIAL NO. 10/774128			
SUPPLEMENTAL INFORMATION DISCLOSURE CITATION				APPLICANT James M. Thommes			
				FILING DATE February 5, 2004			
				GROUP 2838			
U.S. PATENT DOCUMENTS							
EXAM. INIT.	REF	PATENT NUMBER	DATE	NAME	FILING DATE		
		4,493,040	1/8/85	Vanderhelst			
		4,521,672	6/4/85	Fronius			
		5,345,375	9/6/94	Mohan			
		5,444,356	8/22/95	Reynolds et al.			
		5,563,777	10/8/96	Miki et al.			
FOREIGN PATENT DOCUMENTS							
EXAM INIT	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS SUB- CLASS	TRANSLATION YES NO	
		JP05111766	07/05/93	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pages, Etc.)							
		Design Issues For a Zero-Voltage-Switched Power Factor Correction Circuit and DC/DC Converter Power By: Y.V. Panov et al. 1993 VPEC Proceedings of the Virginia Power Electronics Seminar, Blackburg, VA Sept. 19-21, 1993 PP 213-224					
		High Power Factor Switching Preregulator Design Optimization Lloyd Dixon Unitrode Corporation					
EXAMINER				DATE CONSIDERED			
EXAMINER: Initial if citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8a)

I hereby certify that this correspondence is,
on the date shown below, being deposited with the U.S. Postal
Service as first class mail in an envelope addressed to:
Commissioner for Patents, P. O. Box 1450 Alexandria, VA 22313-1450

Jan 31, 2005
George R. Corrigan